

### **ABSTRACT OF THE DISCLOSURE**

A transmitter/receiver apparatus has a status checker that checks whether a plurality of ports of different types are active or not and a reference table that holds, among the transmission delay values between the individual ports, the maximum values corresponding to different combinations of active ports. A value read out from the reference table according to the output signal of the status checker is assigned, as the transmission delay value of the transmitter/receiver apparatus, to a base register. This configuration permits efficient communication.